

A

B

C

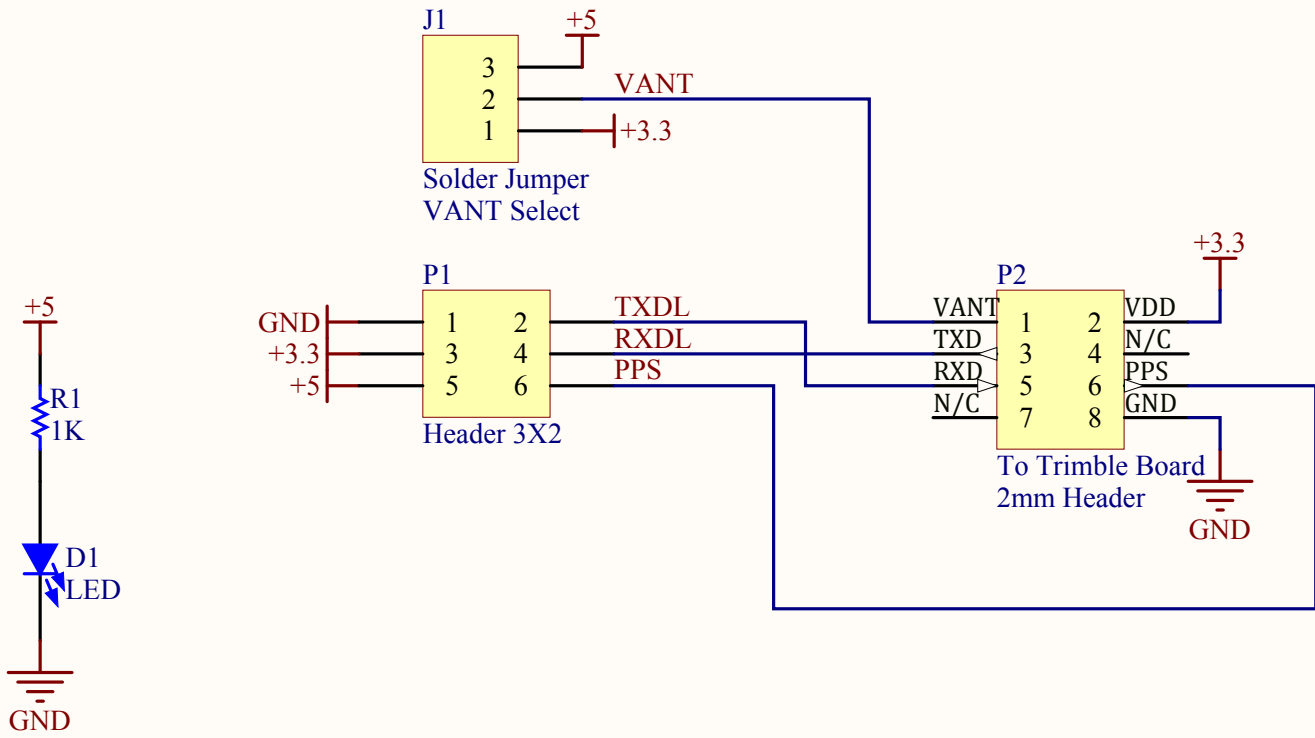
D

A

B

C

D



←17.00mm→

←20.00mm→

Adapter for Resol. T  
For use with Laureline  
GPS NTP Server  
partiallystapled.com

D1



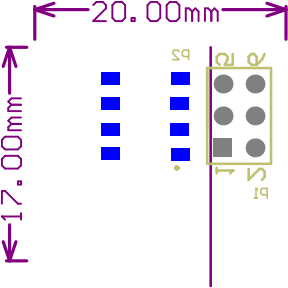
R1



+5.0  
+3.3 re<VANT>  
J1



2012-12



## Design Rules Verification Report

Filename : D:\Users\RadiganConagher\Documents\circuits\laureline\adapter\_resolution\adapter\_resolution.PcbDoc

Warnings 0  
Rule Violations 0

Warnings	
Total	0

Rule Violations	
Short-Circuit Constraint (Allowed=Yes) (InNet('+5')), (InNet('VANT'))	0
Net Antennae (Tolerance=999mm) (All)	0
Silk to Silk (Clearance=0mm) (All), (All)	0
Silkscreen Over Component Pads (Clearance=0mm) (All), (All)	0
Minimum Solder Mask Sliver (Gap=0.076mm) (All), (All)	0
Hole To Hole Clearance (Gap=0.254mm) (All), (All)	0
Hole Size Constraint (Min=0.025mm) (Max=100mm) (All)	0
Height Constraint (Min=0mm) (Max=25.4mm) (Preferred=12.7mm) (All)	0
Width Constraint (Min=0.254mm) (Max=0.254mm) (Preferred=0.254mm) (All)	0
Power Plane Connect Rule(Relief Connect )(Expansion=0.508mm) (Conductor	0
Width=0.254mm)	0
Clearance Constraint (Gap=0.254mm) (All), (All)	0
Un-Routed Net Constraint ( All )	0
Short-Circuit Constraint (Allowed=No) (All), (All)	0
Total	0

## Electrical Rules Check Report

Class	Document	Message
Warning	adapter_resolutiont.SchDoc	Net TXDL has no driving source (Pin P1-2,Pin P2-5)