





$W2$



GND
VCC

TXD
RXD

RTS

CTS



R2535 DTE ADAPTER
2013-05

partially

stabled.com

Design Rules Verification Report

Filename : D:\Users\RadiganConagher\Documents\circuits\laureline\adapter_thunderbolt\adapter_thunderbolt.PcbDoc

Warnings 0
Rule Violations 0

Warnings	
Total	0

Rule Violations	
Net Antennae (Tolerance=0mil) (All)	0
Silk to Silk (Clearance=1mil) (All),(All)	0
Silkscreen Over Component Pads (Clearance=1mil) (All),(All)	0
Minimum Solder Mask Sliver (Gap=0mil) (All),(All)	0
Hole To Hole Clearance (Gap=10mil) (All),(All)	0
Height Constraint (Min=0mil) (Max=10000mil) (Preferred=500mil) (All)	0
Hole Size Constraint (Min=13mil) (Max=250mil) (All)	0
Minimum Annular Ring (Minimum=7mil) (All)	0
Un-Routed Net Constraint ((All))	0
Short-Circuit Constraint (Allowed=No) (All),(All)	0
Power Plane Connect Rule(Relief Connect)(Expansion=20mil) (Conductor Width=10mil) (Air	0
Width Constraint (Min=10mil) (Max=10mil) (Preferred=10mil) (All)	0
Clearance Constraint (Gap=10mil) (All),(All)	0
Total	0

Electrical Rules Check Report

Class	Document	Message
Warning	adapter_thunderbolt.SchDoc	Net RTSL has no driving source (Pin P1-7,Pin R2-2,Pin U1-10)
Warning	adapter_thunderbolt.SchDoc	Net TXDL has no driving source (Pin P1-2,Pin R1-1,Pin U1-11)