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|------------------------|----------------|---------------|---|--|
| Title PPS Piggy | | | partially stapled productions partiallystapled.com | |
| Size: A4 | Number: 1 | Revision: 5.3 | | |
| Date: 2014-03-17 | Time: 23:11:35 | Sheet 1 of 1 | | |
| File: main.SchDoc | | | | |

Design Rules Verification Report

Filename : D:\AD10\projects\pps_piggy\board.PcbDoc

Warnings 0
Rule Violations 0

| Warnings | |
|----------|---|
| Total | 0 |

| Rule Violations | |
|---|---|
| Width Constraint (Min=0.203mm) (Max=0.305mm) (Preferred=0.203mm) (All) | 0 |
| Net Antennae (Tolerance=10mm) (All) | 0 |
| Silk to Silk (Clearance=0mm) (All),(All) | 0 |
| Silk To Solder Mask (Clearance=0mm) (IsPad),(All) | 0 |
| Minimum Solder Mask Sliver (Gap=0.076mm) (All),(All) | 0 |
| Hole To Hole Clearance (Gap=0.254mm) (All),(All) | 0 |
| Height Constraint (Min=0mm) (Max=25.4mm) (Preferred=12.7mm) (All) | 0 |
| Hole Size Constraint (Min=0.025mm) (Max=10mm) (All) | 0 |
| Minimum Annular Ring (Minimum=0.178mm) (All) | 0 |
| Un-Routed Net Constraint (All) | 0 |
| Short-Circuit Constraint (Allowed=No) (All),(All) | 0 |
| Power Plane Connect Rule(Relief Connect)(Expansion=0.508mm) (Conductor | 0 |
| Width=0.254mm) (All),(All) | 0 |
| Clearance Constraint (Gap=0.203mm) (Not IsPad),(Not IsPad) | 0 |
| Clearance Constraint (Gap=0.254mm) (OnLayer('MidTop') or OnLayer('MidBot')),(All) | 0 |
| Clearance Constraint (Gap=0.6mm) (InPolygon),(IsPad and OnMultiLayer and PadIsPlated) | 0 |
| Clearance Constraint (Gap=0.457mm) (InNetClass('50R')),(IsRegion) | 0 |
| Net Antennae (Tolerance=25.4mm) (InNet('VANT') or InNet('RX')) | 0 |
| Short-Circuit Constraint (Allowed=Yes) (InNet('+5')),(InNet('VANT')) | 0 |
| Short-Circuit Constraint (Allowed=Yes) (InNet('RX')),(InNet('RX_AB')) | 0 |
| Total | 0 |

Electrical Rules Check Report

| Class | Document | Message |
|---------|-------------|--|
| Warning | main.SchDoc | Net NetP2_6 has no driving source (Pin P2-6,Pin R3-1) |
| Warning | main.SchDoc | Net NetP2_8 has no driving source (Pin P2-8,Pin R4-1) |
| Warning | main.SchDoc | Net RX has no driving source (Pin J2-2,Pin P1-5) |
| Warning | main.SchDoc | Net USBCFG has no driving source (Pin D2-2,Pin U2-6,Pin U4-15) |